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Defect/Fault Tolerant Systems and
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Students are responsible for:
homework and 1-page paper
summaries - 35% of grade
midterm exam - 20% of grade final
exam - 25% of grade individual or
group project (due at beginning of
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ECE 254 / CPS 225 - Fault-
Tolerant and Testable Computing

...

Objective: To provide students
with an understanding of fault
tolerant computers, including both
the theory of how to design and

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evaluate them and the practical knowledge of real fault tolerant systems.

ECE/CS 554 - Fault-Tolerant and Testable Computing Systems

Another variation of this problem is when fault tolerance in one component prevents fault detection in a different component. For example, if component B performs some operation based on the output from component A, then fault tolerance in B can hide a problem with A.

Fault tolerance - Wikipedia

Fault tolerant and fault testable hardware design March 1985.

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Parag K. Lala. Syracuse Univ., Syracuse, NY. Publisher: Prentice-Hall, Inc. Division of Simon and Schuster One Lake Street Upper Saddle River, NJ; United States; ISBN: 978-0-13-308248-7. Available at Amazon.

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